Pixel updates for MC & Prompt/ReRECO

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DIGI-RECO MC Production

Two conditions updated:

- Bad module list
- Dynamic inefficiency simulation

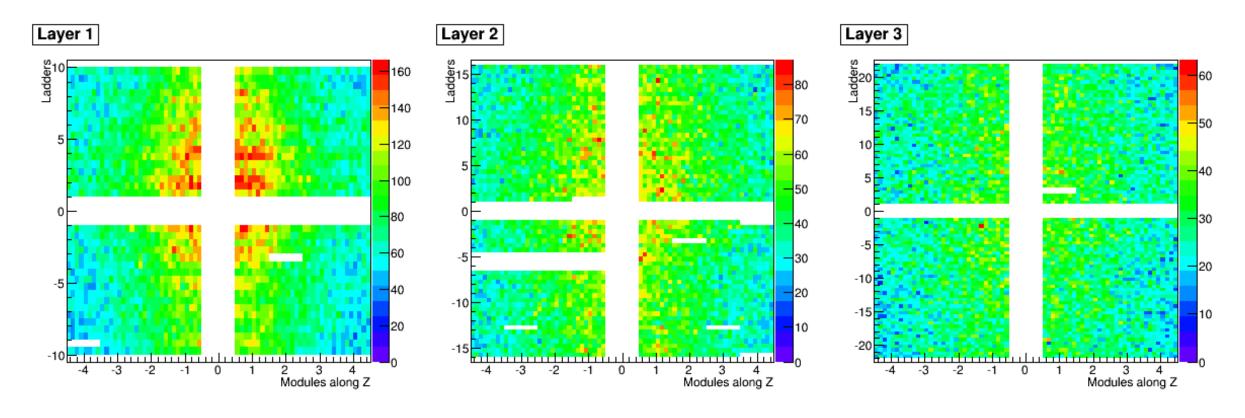
Both of them have effect in the DIGI step





SiPixelQuality

SiPixelQuality_v36_mc



Detector status from 2016-07-07 until so far





Difference between v28 and v36

v36 v28 2015-09-04 2016-07-07 -2 14 bad whole module 12 bad whole module +3 2 bad half module 5 bad half module +1 0 bad ROC 1 bad ROC





SiPixelQuality_v36_mc

Uploaded to Prod on 2016-08-06

Queued to Runll queues on 2016-08-06





Dynamic Inefficiency

SiPixelDynamicInefficiency_13TeV_v3_mc

Presentation by Janos on the Tracker DPG Meeting (<u>link</u>) Presentation by me on the last AlCaDB Meeting (<u>link</u>)

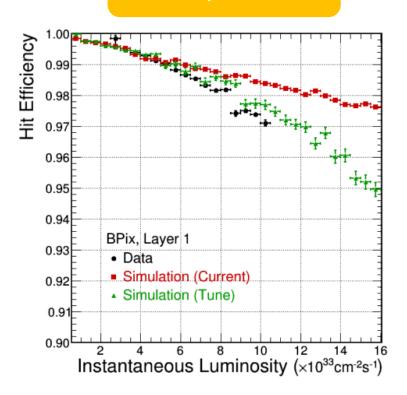
New double column efficiency parametrization Convert hit efficiency to simulated double column loss, Then fit with 2nd (layer 1 only) and 1st order polinomial



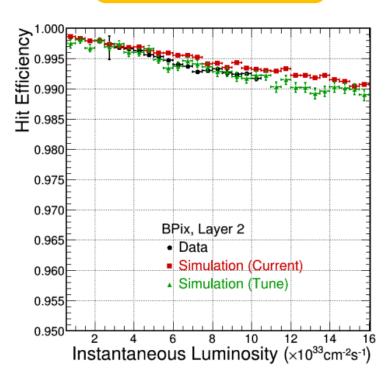


SiPixelDynamicInefficiency_13TeV_v3_mc

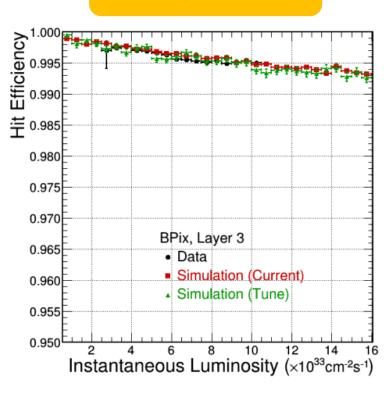
Layer 1



Layer 2



Layer 3







SiPixelDynamicInefficiency_13TeV_v3_mc

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Prompt/ReRECO

Data reco news

Gain calibration carried out last week Fast validation was asked yesterday TSG was contacted today

We foresee to have new Lorentz Angle/GenError/Template payloads



Timeline

1 calibration at each Technical Stop

1 pending gain update and LA/GenErr/Template update in max 2 week

2 foreseen updates during TS2 and TS3



CDC ALCARECO production

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Anirban asked me to inquire about the CDC ALCARECO production

Request to ask PdmV to reprocess the /NoBPTX/Run2016*v2/RAW [*=C,D,E] datasets and produce the ALCARECO output.

Gaelle agreed to be the tracker contact to follow up with the production



