

Status Report on Pixel Phase-I conditions

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Conditions

- ▶ Pixel Phase I Upgrade MC tags

- ▶ Twiki: <https://twiki.cern.ch/twiki/bin/viewauth/CMS/SiPixelPhaseIMC>

SiPixelQuality – MC tags

- ▶ Record: **SiPixelQualityFromDbRcd**
- ▶ Tag: **SiPixelQuality_phase I_ideal**
- ▶ Uploaded to Prod as single tag on 2015-11-10
- ▶ Perfect detector status

SiPixelLorentzAngle – MC tags

- ▶ Record: **SiPixelLorentzAngleSimRcd**
- ▶ Tag: **SiPixelLorentzAngleSim _phaseI_mc_vI**
- ▶ Tag: **SiPixelLorentzAngle _phaseI_mc_vI**
- ▶ Tag: **SiPixelLorentzAngle _fromAlignment_phaseI_mc_vI**
- ▶ Tag: **SiPixelLorentzAngle _forWidth_phaseI_mc_vI**
- ▶ Uploaded to Prod as single tag on 2015-11-10
- ▶ LA values are the same as for the present detector

SiPixelGenError – MC tags

- ▶ Record: **SiPixelGenErrorDBObjectRcd**
- ▶ Tag: **SiPixelGenErrorDBObject_phaseI_38T_mc_v1**
- ▶ Tag: **there is no 0T _mc tag yet**
- ▶ Uploaded to Prod as single tag on 2015-11-10
- ▶ Same as of the present detector

SiPixelTemplate – MC tags

- ▶ Record: `SiPixelTemplateDBObjectRcd`
- ▶ Tag: `SiPixelTemplateDBObject_phaseI_38T_mc_v1`
- ▶ Tag: there is no `0T_mc` tag yet
- ▶ Uploaded to Prod as single tag on 2015-11-10
- ▶ Same as of the present detector

SiPixelGainCalibration – MC tags

- ▶ Record:

SiPixelGainCalibrationOfflineSimRcd

SiPixelGainCalibrationForHLTSimRcd

- ▶ Description:

- ▶ ...HLT... - gains/pedestals are per column,
- ▶ ...Offline... - gains are per column, pedestals are per pixel

SiPixelGainCalibration – MC tags

- ▶ Tag: SiPixelGainCalibrationSim _phaseI_ideal_v2
 - ▶ Tag: SiPixelGainCalibration _hlt_phaseI_ideal_v2
 - ▶ Tag: SiPixelGainCalibration _hlt_phaseI_mc_v2
 - ▶ Tag: SiPixelGainCalibration _phaseI_ideal_v2
 - ▶ Tag: SiPixelGainCalibration _phaseI_mc_v2
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- ▶ Uploaded to Prod as single tag on 2016-02-09
 - ▶ Front end analog response as measured from the new modules

SiPixelFedCabling– MC tags

- ▶ Record: SiPixelFedCablingMapRcd
- ▶ Tag: SiPixelFedCablingMap _phaseI_vI
- ▶ Uploaded to Prod as single tag on 2016-02-09
- ▶ Content is irrelevant
because of digi2raw <-> raw2digi

SiPixelDynamicInefficiency – MC tags

- ▶ Record: **SiPixelDynamicInefficiencyRcd**
- ▶ Tag: **SiPixelDynamicInefficiency_PhaseI_v1**
- ▶ Uploaded to Prod as single tag on 2016-03-02
- ▶ Payload with DynIneff off
(constant 0.999 factors everywhere)

Backup

Backup slides



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